EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L2	4500	((plug adj board) plugboard board slot) same capacitor and capacitor with (delay$4 start$3 powering)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/12/22 08:14
L3	2166	((plug adj board) plugboard board slot) with capacitor and capacitor with (delay$4 start$3 powering)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/12/22 08:14
L4	522	3 and comparator	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/12/22 08:46
L5	509	307/109.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/12/22 08:55
L7	6	(start$3 slot capacitor delay).clm.	US-PGPUB	AND	ON	2008/12/22 08:57
S1	1	("5910690").PN.	USPAT; USOCR	OR	OFF	2008/08/12 16:11
S2	917	comparator and capacitor same time near3 delay$3 and resistor and voltage and start$3 near5 current	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/08/12 16:15
S3	110	307/109.ccls. and start$4	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/08/12 16:16
S4	46	307/109.ccls. and start$4 with current	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/08/12 16:17

S5	1855	capacitor with (plug$3 board slot) and start$4 with current	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/08/12 16:19
S6	359	S5 and comparator and capacitor and resistor and voltage and start$3 near5 current	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/08/12 16:20
S7	784	Schenk and capacitor	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/08/12 16:25
S8	209	Schenk and capacitor and delay$3	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/08/12 16:25
S9	72	Schenk and capacitor and delay$3 and current and comparator	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/08/12 16:25
S10	576	capacitor with board with (slot plug$3)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/08/12 16:29
S11	47	S10 and delay$3	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/08/12 16:29
S12	27	("3993935" | "4079440" | "4245270" | "4454552" | "4510553" | "5077675" | "5210855" | "5272584"). PN. OR ("5384492"). URPN.	US-PGPUB; USPAT; USOCR	OR	OFF	2008/08/12 16:33

S13	26859	(circuit capacitor) with (wire wiring wired configur$3 configurat$3) with (holes slots sockets plug$3) and (time delay$3)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/08/12 16:37
S14	8281	S13 and capacitor	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/08/12 16:41
S16	838	(capacitor) with (wire wiring wired configur$3 configurat$3) with (slots sockets plug$3) and (time delay$3) and capacitor same (time delay$3)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/08/12 16:42
S17	0	("2007/0108844").URPN.	USPAT	OR	OFF	2008/08/12 16:43
S18	34	(capacitor) with (wire wiring wired configur$3 configurat$3) with (slots sockets plug$3) and (time near2 delay$3) and capacitor same (time near2 delay$3)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/08/12 16:45
S19	285	(capacitor) with (slots socket plugboard (plug adj board)) and (time near2 delay$3)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/08/12 16:50
S20	1476	(capacitor) with configur$5 and start$3 near3 current and (varies varying vary)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/08/12 17:01
S21	746	S20 and (socket plu$3 slot)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/08/12 17:01
S22	363	S20 and (socket plu$3 slot) same capacitor	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/08/12 17:04

S23	495	439/43.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/08/12 17:12
S24	40	S23 and capacitor	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/08/12 17:12
S25	1	("3411097").URPN.	USPAT	OR	OFF	2008/08/12 17:26
S26	1496	439/43-54.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/08/12 17:27
S27	0	S26 and comparator and capacitor same time near3 delay$3 and resistor and voltage	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/08/12 17:27
S28	2	S26 and comparator and capacitor and resistor and voltage	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/08/12 17:28
S29	109	((plug adj board) plugboard) and capacitor same (delay$4 (start$3 near2 current))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/08/12 17:30

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